

What is claimed is:

1. An active matrix liquid crystal display device comprising:
a pair of substrates with a liquid crystal layer therebetween;
a plurality of video signal lines and a plurality of scan lines formed on one of the pair of substrates, and a plurality of pixel electrodes connected to one of the video signal lines through an active device; and
a plurality of color filters formed on another of the pair of substrates;
wherein a shield electrode overlaps with the one of the video signal lines in plane view and is arranged between the one of the video signal lines and one of the color filters, and a planarization layer is arranged between one of the pixel electrodes and the shield electrode.
2. An active matrix liquid crystal display device according to claim 1, wherein the shield electrode which overlaps with the one of the video signal lines is at least $9.4\text{ }\mu\text{m}$ wider than a width of the one of the video signal lines.
3. An active matrix liquid crystal display device according to claim 1, wherein the shield electrode serves as a reference electrode.
4. An active matrix liquid crystal display device according to claim 3, wherein the shield electrode overlaps with both of the one of the video signal lines and one of the scan lines.
5. An active matrix liquid crystal display device according to claim 4, wherein the shield electrode is elongated along the one of the video signal lines at a region of overlap with the one of the video signal lines and is elongated along the one of the scan lines at a region of overlap with the one of the scan lines.

6. An active matrix liquid crystal display device according to claim 5, wherein the shield electrode includes first portions which overlap with respective ones of the video signal lines and a second portion formed between adjacent first portions in a pixel region.

7. An active matrix liquid crystal display device according to claim 6, wherein the first portions of the shield electrode have a width which is at least $9.4\ \mu\text{m}$ wider than a width of the one of the video signal lines.

8. An active matrix liquid crystal display device according to claim 6, wherein the shield electrode is made of transparent conductor.

9. An active matrix liquid crystal display device according to claim 8, wherein the shield electrode is made of ITO.

10. An active matrix liquid crystal display device according to claim 3, wherein the shield electrode overlaps with the active device.

11. An active matrix liquid crystal display device according to claim 10, wherein the liquid crystal display device is an in-plane switching mode liquid crystal display device.

12. An active matrix liquid crystal display device according to claim 8, wherein the liquid crystal display device is an in-plane switching mode liquid crystal display device.

13. An active matrix liquid crystal display device comprising:
a pair of substrates with a liquid crystal layer therebetween;
a plurality of video signal lines and a plurality of scan lines formed on one of the pair of substrates, and a plurality of pixel electrodes connected to one of the video signal lines through an active device; and
a reference electrode which overlaps with the one of the video signal lines in plane view, the reference electrode having a matrix shape.

14. An active matrix liquid crystal display device according to claim 13, wherein the reference electrode is arranged between the one of the video signal lines and a color filter.

15. An active matrix liquid crystal display device according to claim 14, further comprising a planarization layer arranged between the pixel electrode and the reference electrode.

16. An active matrix liquid crystal display device according to claim 13, wherein the reference electrode which overlaps with the one of the video signal lines is at least $9.4\ \mu\text{m}$ wider than a width of the one of the video signal lines.

17. An active matrix liquid crystal display device according to claim 13, wherein a width of the reference electrode at a region of overlap with the one of the video signal lines is wider than a width of one of the pixel electrodes.

18. An active matrix liquid crystal display device according to claim 17, wherein the reference electrode overlaps with both of the one of the video signal lines and one of the scan lines.

19. An active matrix liquid crystal display device according to claim 18, wherein the reference electrode is elongated along the one of the video signal lines at a region of overlap with the one of the video signal lines and is elongated along the one of the scan lines at a region of overlap with the one of the scan lines.

20. An active matrix liquid crystal display device according to claim 19, wherein the reference electrode includes first portions which overlap with respective ones of the video signal lines and a second portion formed between adjacent ones of the first portions in a pixel region.

21. An active matrix liquid crystal display device according to claim 20, wherein the first portions of the reference electrode have a width which is at least $9.4\ \mu\text{m}$ wider than a width of the one of the video signal lines.

22. An active matrix liquid crystal display device according to claim 20, wherein the reference electrode is made of transparent conductor.

23. An active matrix liquid crystal display device according to claim 22, wherein the reference electrode is made of ITO.

24. An active matrix liquid crystal display device according to claim 23, wherein the reference electrode overlaps with the active device.

25. An active matrix liquid crystal display device according to claim 24, wherein the liquid crystal display device is an in-plane switching mode liquid crystal display device.

26. An active matrix liquid crystal display device according to claim 17, wherein the reference electrode is arranged between the one of the video signal lines and a color filter.

27. An active matrix liquid crystal display device according to claim 26, further comprising a planarization layer arranged between the pixel electrode and the reference electrode.

28. An active matrix liquid crystal display device according to claim 13, wherein the reference electrode includes first portions which overlap with respective ones of the video signal lines and a second portion formed between adjacent ones of the first portions in a pixel region, a width of the first portions being wider than width of the second portion.

29. An active matrix liquid crystal display device according to claim 28, wherein the reference electrode is arranged between the one of the video signal lines and a color filter.

30. An active matrix liquid crystal display device according to claim 29, further comprising a planarization layer arranged between the pixel electrode and the reference electrode.

31. An active matrix liquid crystal display device according to claim 28, wherein the first portions of the reference electrode have a width which is at least 9.4 μm wider than a width of the one of the video signal lines.

32. An active matrix liquid crystal display device according to claim 28, wherein the reference electrode is made of transparent conductor.

33. An active matrix liquid crystal display device according to claim 28, wherein the first portions of the reference electrode is wider than the pixel electrode.

34. An active matrix liquid crystal display device according to claim 33, wherein the reference electrode overlaps with both of the one of the video signal lines and the one of the scan lines.

35. An active matrix liquid crystal display device according to claim 34, wherein the reference electrode is made of transparent conductor.

36. An active matrix liquid crystal display device according to claim 35, wherein the reference electrode is made of ITO.

37. An active matrix liquid crystal display device according to claim 36, wherein the reference electrode is arranged between the one of the video signal lines and a color filter.

38. An active matrix liquid crystal display device according to claim 37, further comprising a planarization layer arranged between the pixel electrode and the reference electrode.